

Notice of Allowability

Application No.

09/935,686

Examiner

Ted T. Vo

Applicant(s)

KAMIYA ET AL.

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2192

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 11/23/04.
2. ☒ The allowed claim(s) is/are 1-20.
3. ☐ The drawings filed on _____ are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

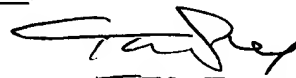
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


TED T. VO
Primary Examiner

1. This communication is in response to the amendment filed on 11/23/04, responsive to the Office action dated on 06/10/2004.

The amendment and the arguments to the rejection under prior art of record, Mitsubishi (US Pat. No. 6,745,320 B1), have been fully considered and overcome the rejection. Mitsubishi remains the closest art of record.

Reasons for Allowance

2. Claims 1-20 are allowed.

Prior art of record, Mitshushi, discloses generation of object code to be executed by a CPU, where objects code includes bit test instructions for judging. When executing an instruction included with test bits, the bit test instruction causes a flag to be set. For example, "The bit test instruction inspects predetermined bits of data on the general purpose register or on the address space and reflects the inspection result to Z flag of CCR". The bit test instruction causes a branch decision based on its true or false tested value.

However, Applicants point out that (in regard to the amended limitation of independent Claims 1, 4, 6, 8, 10-12) the existence of specific instruction in Mitsubishi fails to teach or suggest generating object code from source code, and Mitsubishi fails to disclose generating object code from input source code where a bit operation instruction in the source code is expressed as a condition judgment expression (Remarks: page 13, last paragraph). Applicants point out that Mitsubishi does not discloses, right side portion of the bit operation expression is expressed in the object code as a condition judgment expression, and does not disclose being expressed as instructions in the object code that selectively assign a predetermined first binary value and a predetermined second binary value in accordance with whether a true or false decision obtained from the condition judgment expression (Remarks: page 13, lines 3-12).

Therefore, the following is an examiner's statement of reasons for allowance: The cited prior arts taken alone or in combination fail to teach claimed invention, compilers, computer readable media, program conversion methods, microcomputers, comprising at least features,

"a right-side portion of said bit operational expression in said source code being expressed in object code as a condition judgment expression that is in accordance with Boolean logic, said condition judgment expression providing a result that is "true" when said combination of respective values has a predetermined first Boolean logic relationship and a result that is "false" when said combination of respective values has a predetermined second Boolean logic relationship, and being expressed as instructions in the object code that selectively assign a predetermined first binary value and a predetermined second binary value that is inverse of said first binary value to a bit variable which holds a result of said bit operational expression, in accordance with whether a "true" or a "false" decision is obtained from said condition judgement expression"

as recited in independent Claim 1 and in such manners in independent Claims 4, 6, 8, 10-12.

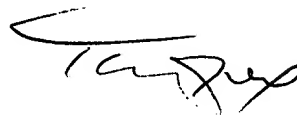
3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ted T. Vo whose telephone number is (571) 272-3706. The examiner can normally be reached on 8:00AM to 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3694. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application should be directed to the TC 2100 Group receptionist: 571-272-2100. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Ted T. Vo', with a stylized flourish at the end.

Ted T. Vo
Primary Examiner
Art Unit 2192
April 11, 2005